

 Form PTO-1449		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE			ATTY. DOCKET NO. MI22-1533	SERIAL NO. 09/754,926	
		LIST OF ART CITED BY APPLICANT (Use several sheets if necessary)			APPLICANT Kic Y. Ahn et al.	FILING DATE January 4, 2001	GROUP 2813
U.S. PATENT DOCUMENTS							
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
<i>EK</i>	AA	6,093,612	07/00	Suh	—	—	
	AB						
	AC						
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FOREIGN PATENT DOCUMENTS							
		Document Number	Date	Country	Class	Subclass	Translation
							Yes
	AM						No
	AN						
	AO						
	AP						
	AQ						
OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)							
<i>EK</i>	AR	Wolf, S., "Silicon Processing for the VLSI Era", Vol. 2: Process Integration, 1990 Lattice Press, Sunset Beach, CA,					
		pp. 332-333.					
	AS						
	AT						
EXAMINER <i>Er. K. K. K.</i>			DATE CONSIDERED <i>2/1/03</i>				
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.							